

Building Energy Efficiency Higher & Advanced Network (BHAVAN) Awards 2016

The *Department of Science and Technology (DST)*, Government of India and the *Indo-US Science and Technology Forum (IUSSTF)* are pleased to announce the candidates selected for the **Building Energy Efficiency Higher & Advanced Network (BHAVAN) Program 2016**.

Candidates selected for BHAVAN Internships

Sr. No	NAME	PARENT INSTITUTION
1	Maaz Barin Dixit	CEPT University
2	Karthik Panchabikesan	College of Engineering, Anna University
3	Brijesh Pandey	IIT-Bombay
4	S. Manikandan	IIT-Delhi
5	Arjun Deo	IIT-Delhi
6	Aviruch Bhatia	IIIT-Hyderabad

Candidates selected for BHAVAN Fellowships

Sr. No	NAME	PARENT INSTITUTION
1	Mahanth Prasad	CSIR-CEERI
2	Ronita Bardhan	IIT-Bombay
3	Naran Manji Pindoriya	IIT-Gandhinagar
4	Sudipta Halder	NIT Silchar
5	Jawar Singh	PDPM-IIIT Jabalpur
6	Ashok K. Loganathan	PSG College of Technology
7	Rahul V. Ralegaonkar	VNIT-Nagpur
8	Sanyogita Manu	CEPT University